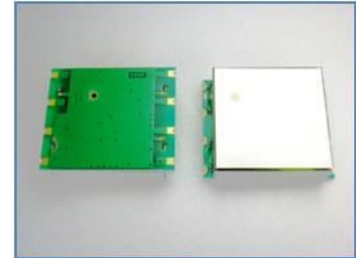


LVPECL VCXO

VXSW-07S5 Series

◆特長・用途/Feature

- SMD Package
- Low Phase noise
- Supply Voltage **+3.3V**
- Output Level **LVPECL**
- Frequency range **100MHz ~ 320MHz**



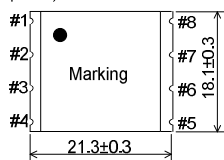
◆仕様/Specifications

RoHS Compliant

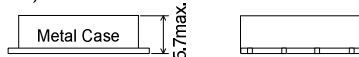
項目 / item		Min	Max	単位 / unit	Note
出力周波数 Frequency range		100	320	MHz	--
動作温度範囲 Operating Temp. range		0	+70	°C	--
電源電圧 Power Supply voltage		+3.3		V	--
消費電流 Current		--	100	mA	--
周波数安定度 Freq. Overall tolerance		±30		10 ⁻⁶	Inclusive of initial tolerance , operating temperature , supply voltage change.
周波数可変範囲 Freq. control range		±40	--	10 ⁻⁶	Control Voltage(VC) : +0V~+3.3V Input impedance : 56kΩ min.
出力レベル Output level	VOH	+2.22	--	V	--
	VOL	--	+1.70	V	--
Rise time , Fall time		--	2	ns	+1.77V⇔+2.22V
Duty Cycle		40	60	%	at +1.96V
位相雑音 Phase noise (例:122.88MHz)		--	-78	dBc/Hz	10Hz Offset
		--	-110	dBc/Hz	100Hz Offset
		--	-125	dBc/Hz	1kHz Offset
		--	-142	dBc/Hz	10kHz Offset
		--	-142	dBc/Hz	100kHz Offset

◆外形寸法/Dimensions unit : mm

(Top View)



(Side View)



(Bottom View)



Terminal Connections

Terminal#	Connection
1	GND
2	GND
3	GND
4	Vc or NC
5	GND
6	Output
7	Output
8	VDD

Pin#4 is NC for SPXO/TCXO

◆Phase noise DATA

